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2814

PATENT 97-CC-240 DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

PHILIPPE GAYET, et al.

Serial No.: 10/046,322

Filed: October 23, 2001

For: INTEGRATED CIRCUIT WITH

STOP LAYER AND ASSOCIATED

FABRICATION PROCESS

)

Group Art Unit: 2814

Examiner: P. Cao

RESPONSE

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated August 5, 2002, in connection with the above-identified application, please enter and consider the following remarks.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents,

Washington, D.C. 20231, on

Date of Denosit

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Signature (

Dated